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L1	17	(("7227558") or ("5353234") or ("5038294") or ("5646862") or ("6289254") or ("7188072") or ("7200564") or ("6505204") or ("5987458")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/29 19:47
L2	4831	((700/96,97,182,98,99,104,106) or (703/1)).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/29 19:48
L3	73	2 and (subset with components)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/29 19:49
L4	11	("5523960" | "5907488" | "5980084" | "6223092" | "6434438").PN. OR ("6725184").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/29 19:58
L5	7	("20020156757" | "4638442" | "4862376" | "4875162" | "4912657" | "5822206").PN. OR ("7103434").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/29 20:15
L6	164	(component and subset).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/29 20:20
L7	36	6 and generate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/29 20:15

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1 Automatic generation of logic diagrams   ■
James A. Smith, James G. Linders
June 1976 **Proceedings of the 13th conference on Design automation DAC '76**
Publisher: ACM Press

Full text available: 📄 pdf(890.84 KB)   Additional Information: full citation, abstract, references, citings, index terms

This paper identifies the basic notions involved in the automatic generation of logic
diagrams. A system (ALDGS) for generating logic diagrams automatically is described and
sample diagrams are given.

Keywords: Design automation, Digital systems, Logic layout, Placement, Routing

2 Sequential Operations in Digital Picture Processing   ■
Azriel Rosenfeld, John L. Pfaltz
October 1966 **Journal of the ACM (JACM)**, Volume 13 Issue 4
Publisher: ACM Press
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3 The connectivity carcass of a vertex subset in a graph and its incremental   ■
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Yefim Dinitz, Alek Vainshtein
May 1994 **Proceedings of the twenty-sixth annual ACM symposium on Theory of
computing STOC '94**
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4 Interconnect Architecture: FPGA switch block layout and evaluation   ■
Herman Schmit, Vikas Chandra
February 2002 **Proceedings of the 2002 ACM/SIGDA tenth international symposium
on Field-programmable gate arrays FPGA '02**
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1 Special section: Reasoning about structure, behavior and function   ■
B. Chandrasekaran, Rob Milne
July 1985 **ACM SIGART Bulletin**, Issue 93
Publisher: ACM Press
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The last several years' of work in the area of knowledge-based systems has resulted in a deeper understanding of the potentials of the current generation of ideas, but more importantly, also about their limitations and the need for research both in a broader framework as well as in new directions. The following ideas seem to us to be worthy of note in this connection.

2 Special issue: AI in engineering   ■
D. Sriram, R. Joobbani
April 1985 **ACM SIGART Bulletin**, Issue 92
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The papers in this special issue were compiled from responses to the announcement in the July 1984 issue of the SIGART newsletter and notices posted over the ARPAnet. The interest being shown in this area is reflected in the sixty papers received from over six countries. About half the papers were received over the computer network.

3 Issues and answers in CAD tool interoperability   ■
Mike Murray, Uwe B. Meding, Bill Berg, Yatin Trivedi, Bill McCaffrey, Ted Vucurevich
June 1996 **Proceedings of the 33rd annual conference on Design automation DAC '96**
Publisher: ACM Press
Full text available: 🗎 pdf(62.07 KB)   Additional Information: full citation, index terms

4 Vex—A CAD toolbox   ■
Jules P. Bergmann, Mark A. Horowitz
June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation DAC '99**
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1   <u>Special issue: AI in engineering</u>   ■

D. Sriram, R. Joobbani
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2   <u>Three levels of the wiring interconnection problem</u>   ■

D. K. Frayne
January 1965 **Proceedings of the SHARE design automation project DAC '65**
Publisher: ACM Press
Full text available: 📄 <u>pdf(469.79 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>index terms</u>

> This paper describes a set of computer programs written to aid in the design, documentation, manufacture, and test of electronic hardware. The programs discussed are limited to the areas involved with the problem of wiring interconnection. While it can be said that there are over 10 different levels or types of wiring interconnection, these programs primarily concern the most common three: 1. Interconnection of circuit cards or other modules on a chassis 2. Interconnec ...

3   <u>A computer aided interconnection system</u>   ■

Richard W. Wilson
January 1969 **Proceedings of the 6th annual conference on Design Automation DAC '69**
Publisher: ACM Press
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> When electronic equipment is designed there are two ways of defining the equipment: 1) by parts content and 2) by electrical interconnections. The concern here is with electrical interconnections; how they are defined; how they flow through the phases of design; and how a computer-aided system has helped to create an efficient design tool out of a cumbersome manual data flow. By comparing the manual system (Fig 1) with the computer-aided system (F ...

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